

THE COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, D.C. 20231

Sir:

Transmitted herewith for filing is the Utility patent application of:

INVENTOR:	NAOKI YAMAMOTO
FOR:	FABRICATION PROCESS OF A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

17858 U.S. PTO
10/717705



Enclosed are:

- ☒ 18 sheets of ☐ formal ☐ informal drawings.
- ☒ Transmittal of Japanese application 2002-369422 to claim the priority date of December 20, 2002.
- ☒ An Unexecuted Declaration.
- ☒ An Information Disclosure Statement
- ☒ The filing fee is calculated as shown below:

FOR	NUMBER FILED		NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	20	-20		x\$18 =	0.00
INDEPENDENT CLAIMS	3	-3	2	x\$86 =	0.00
MULTIPLE DEPENDENT CLAIM(S) (if applicable)				+\$290 =	
				BASIC FEE	\$770.00
				TOTAL OF ABOVE	\$770.00
REDUCTION BY <input type="checkbox"/> FOR FILING BY SMALL ENTITY (note 37 C.F.R. § 1.9, 1.27, 1.28). IF APPLICABLE, VERIFIED STATEMENT MUST BE ATTACHED					
				TOTAL	\$770.00

- ☒ A check in the amount of **\$770.00** to cover the filing fees is enclosed.
- ☒ The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to **Deposit Account Number 08-1480**.

Respectfully submitted,

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November 21, 2003

The PTO did not receive the following
listed item(s) Information Disclosure
statement